

GULSHAN MULLA

ELECTRICAL ENGINEER



About Me



March 11, 1995



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Vijayapur, Karnataka



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OBJECTIVE

To work in a competitive and challenging environment where I can use my skill and knowledge to help the company meet and surpass its goals for organizational and personal growth.



Education

Masters, Business Administration

P.G. Halakatti BLDEA,S College of Engineering college,
Vijayapur, Karnataka.

Result

I sem-64%

II sem-73%

III sem-96.77%

Bachelor, Technology

Secab college of Engineering & Technology
Vijayapur, Karnataka

Graduated, July 2017

Marks 69.66%

10+2

Secab Pre-university college
Karnataka State Board
Vijayapur, Karnataka

Graduated, July 2013

Marks 66%



Areas of interest

Power Electronics, Machine Design, Digital Signal Processing, Control System.



Project(s)

TITLE: "ARM Microcontroller based Border Security Robot"(Project during Bachelor's Degree)

DESCRIPTION: This Project uses ARM CortexM3 as the microcontroller LPC1768 and Sensors for detection.

APPLICATION: In defense areas.

TITLE: " IOT ENABLED HOME AUTOMATION SYSTEM FOR SMART HOME APPLICATION"(Project during Masters)

DESCRIPTION: This project uses IoT; arduino uno; webserver; Relays for home automation.

APPLICATION: For smart home automation.



Internship

Company Name : NTPC Ltd, Kudgi Vijayapur

Duration : 15 Days



Workshop's Attended

- 1. PCB Designing:** Participated in workshop on "PCB Designing" organized by Dept. of Electrical and Electronics, Secab I.E.T, Vijayapur
- 2. Electrical Wiring Workshop:** Participated in workshop on "Electrical wiring" organized by Dept. of Electrical and Electronics, Secab I.E.T ,Vijayapur
- 3. PLC SCADA:** Participated in workshop on "PLCSCADA" organized by Dept. of Electrical and Electronics, Secab I.E.T, Vijayapur
- 4. Attended Two weeks faculty development program(FDP) on "MATLAB AND SIMULINK" conducted at Dr. P. G. Halakatti College of Engineering and Technology.**
- 5. Attended VGST Sponsored One Week Faculty Development Program on "Design & Implementation of Hardware Accelerators and Protocols on FPGA" at Dr. P. G. Halakatti College of Engineering and Technology.**



Paper published

- 1. "ARM Microcontroller based Border Security Robot", authors "Mr. Zuber Ahmed Puneekar, Mr. Mohammad Ziaullah , Ms. Gulshan Mulla, Mr. Shantinath Savadi" in "IJSRD -International Journal for Scientific Research & Development |Vol.5,Issue 06, 2017".**
- 2. "IOT ENABLED HOME AUTOMATION SYSTEM FOR SMART HOME APPLICATION", authors "Ms. Gulshan Mulla, Mr. Vinay Bagali" in "IJRAR-International Journal of Research and analytical reviews|Vol.6, Issue 02, June 2019."**



Interests



Reading



Listen Music



Travelling



Movies



Personal Details

- Father's Name: Mr. Rajesab Mulla
- Birthday : March 11, 1995
- Gender : Female
- Marital Status : Single
- Nationality : Indian

Declaration

I, Gulshan, hereby declare that the information contained herein is true and correct to the best of my knowledge and belief.

Gulshan